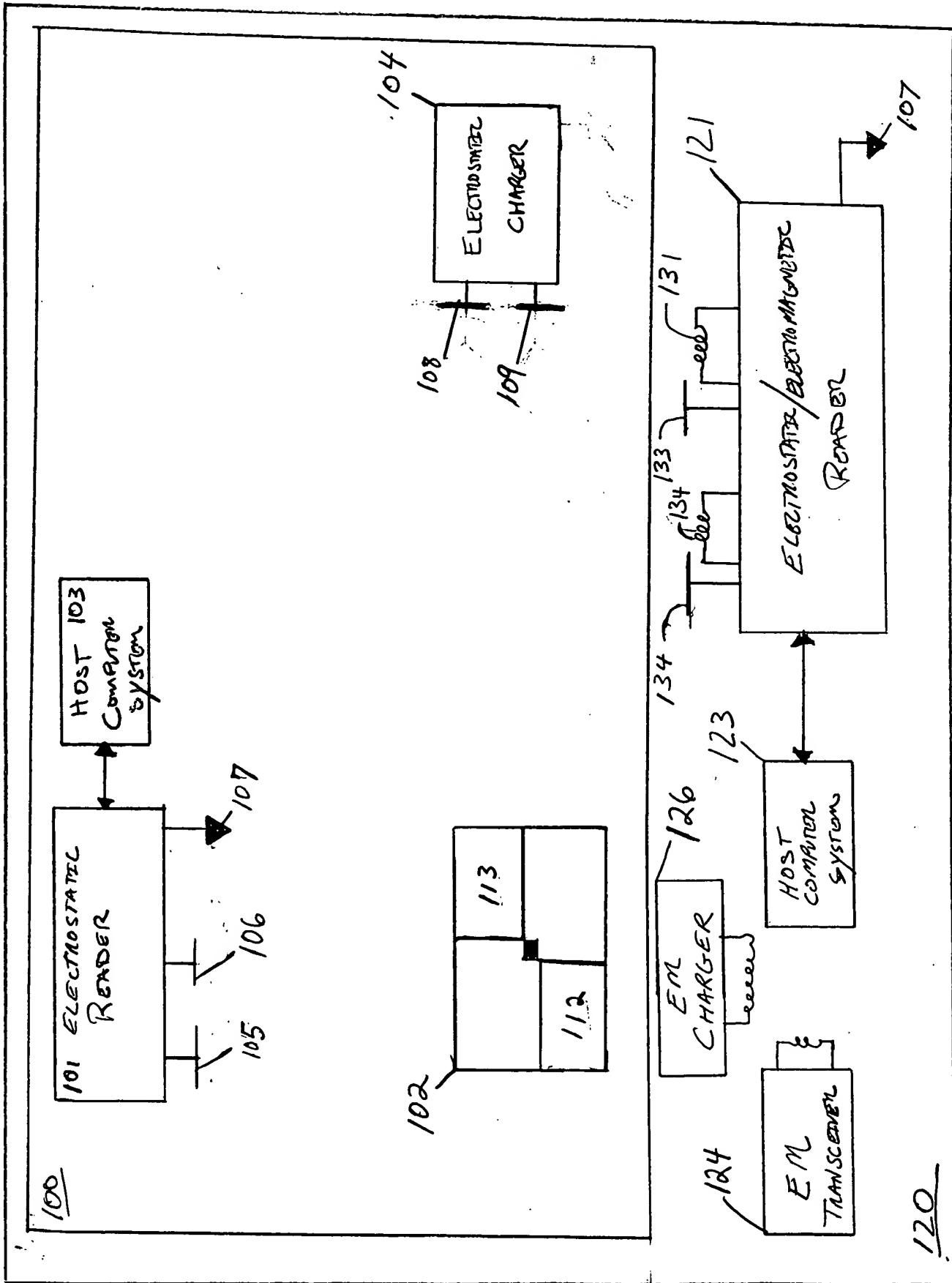


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IND10200
Sh. 1 of 16

FIG. 1

IND 10200

00000000000000000000000000000000

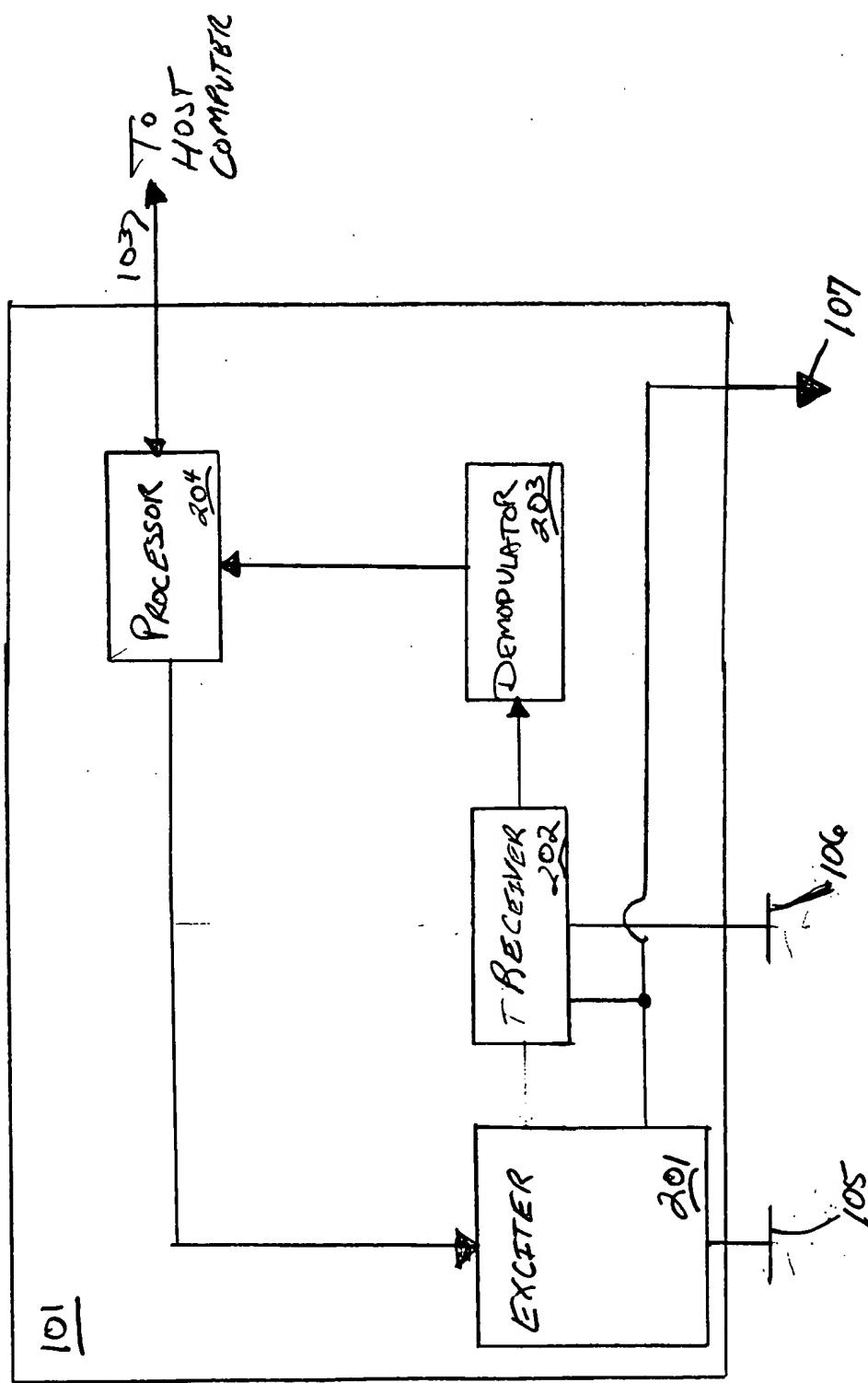
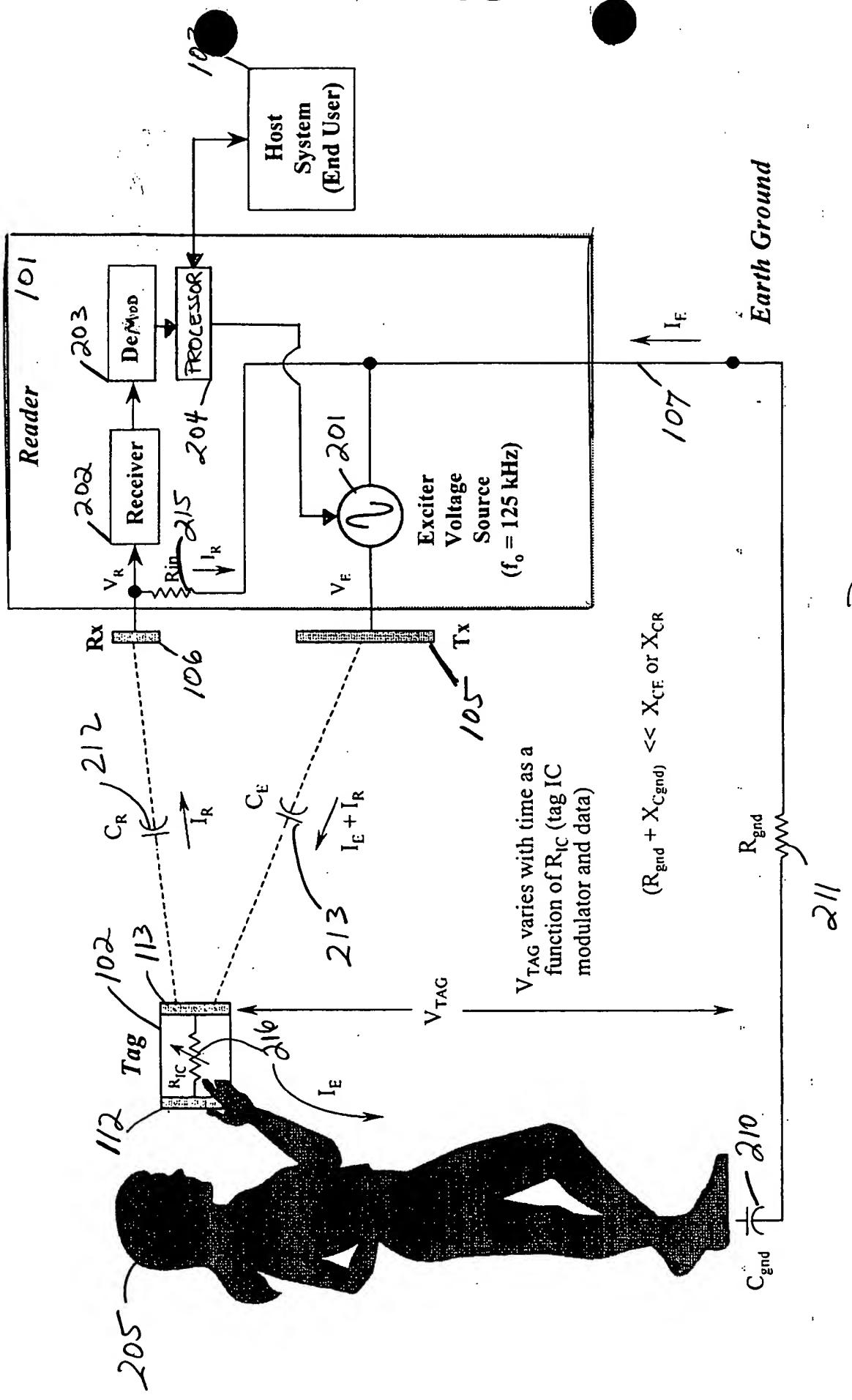


FIG. 2 A

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sh. 2 of 16

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11510200 94.3 04 16

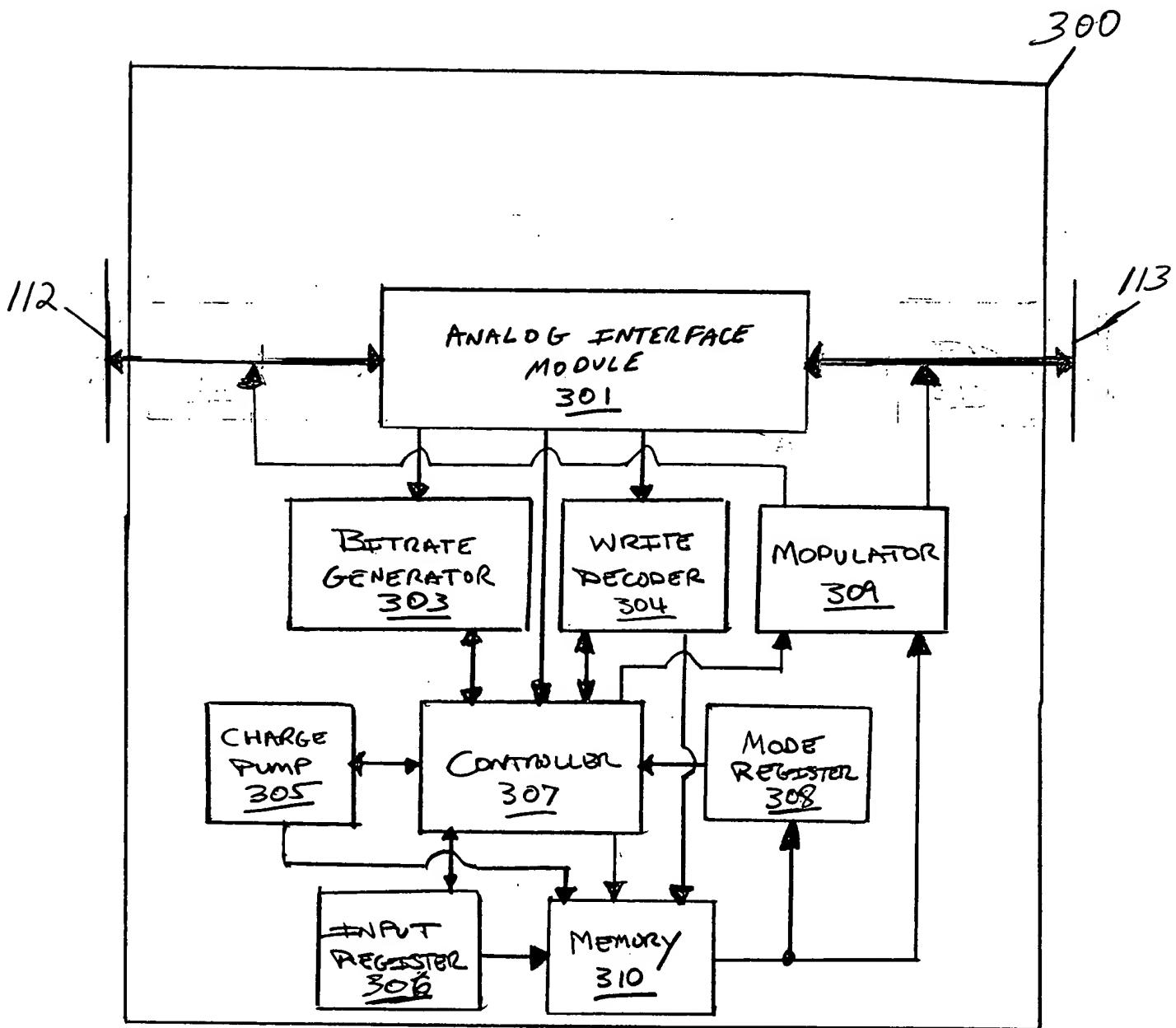
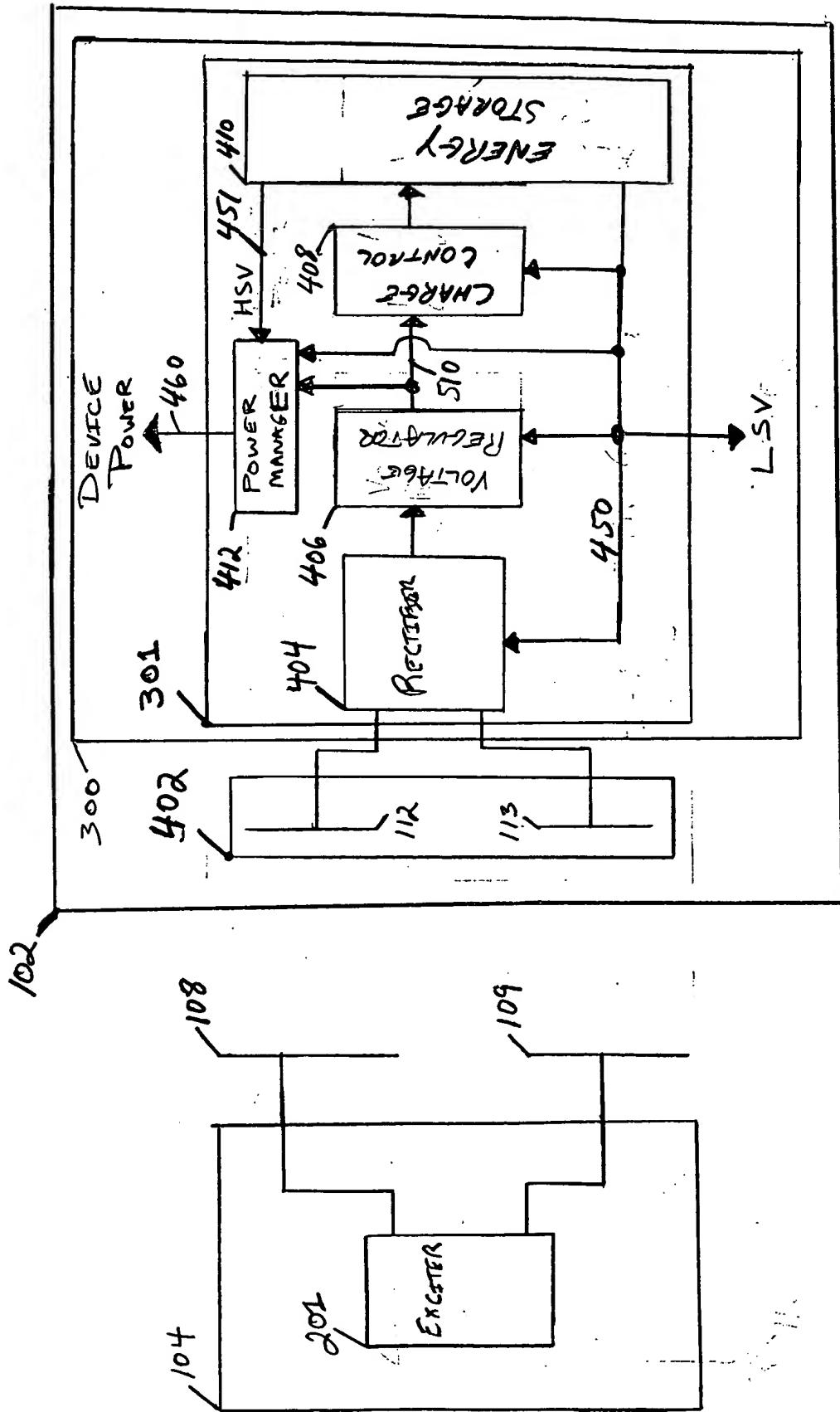


FIG. 3

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IND 10200

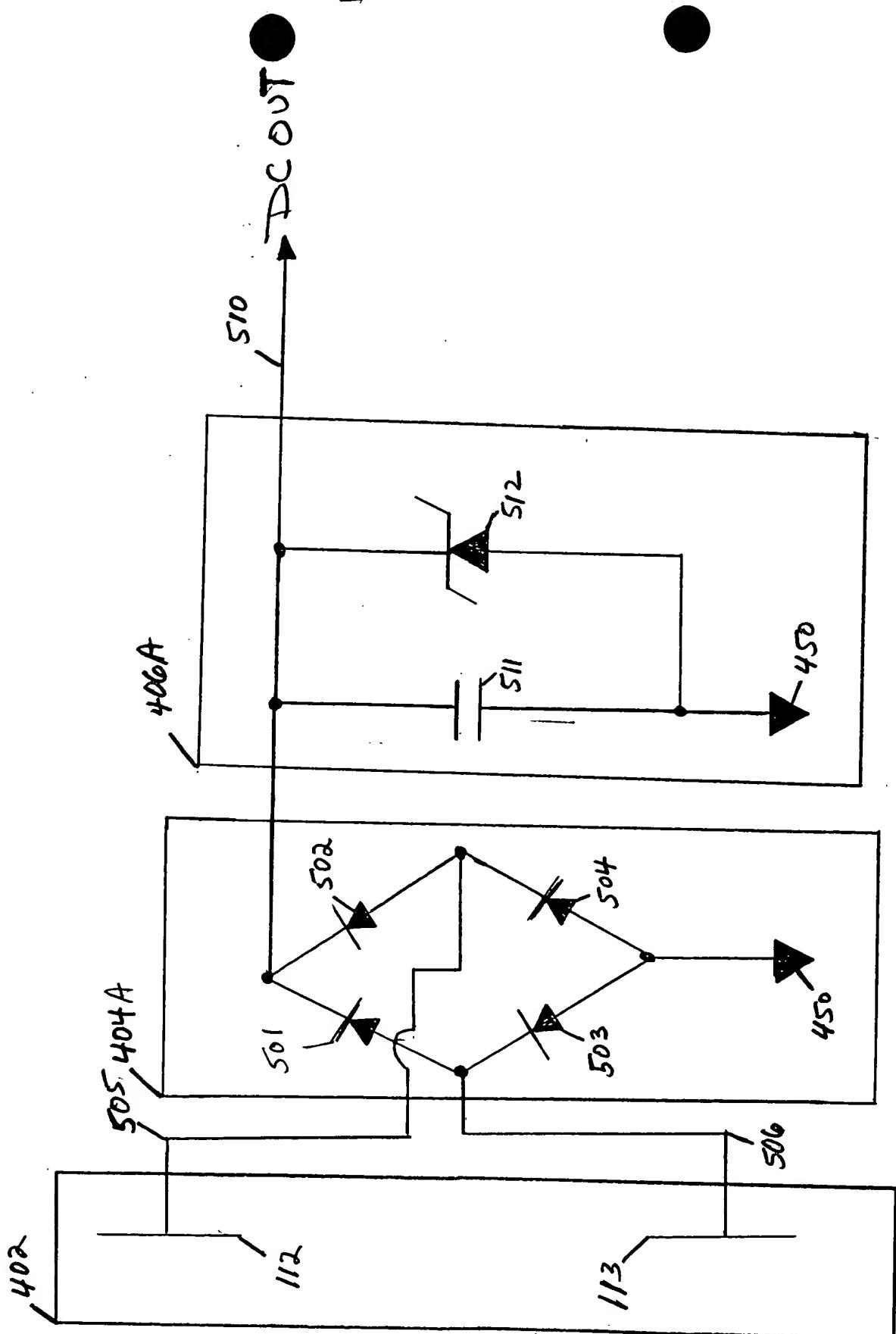
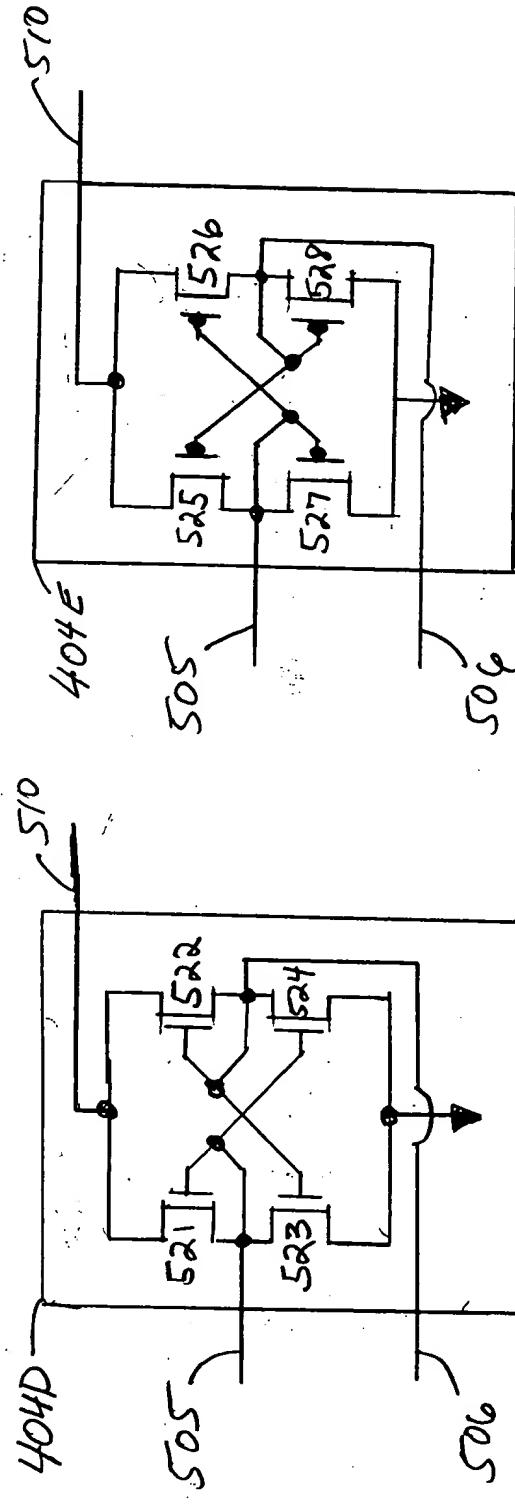
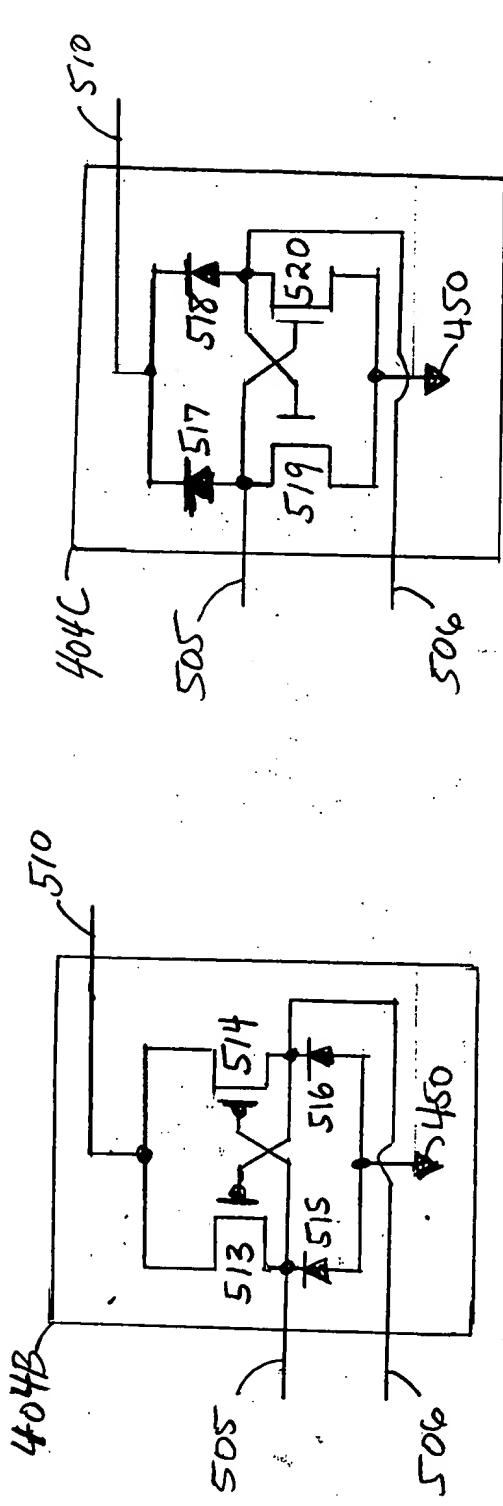
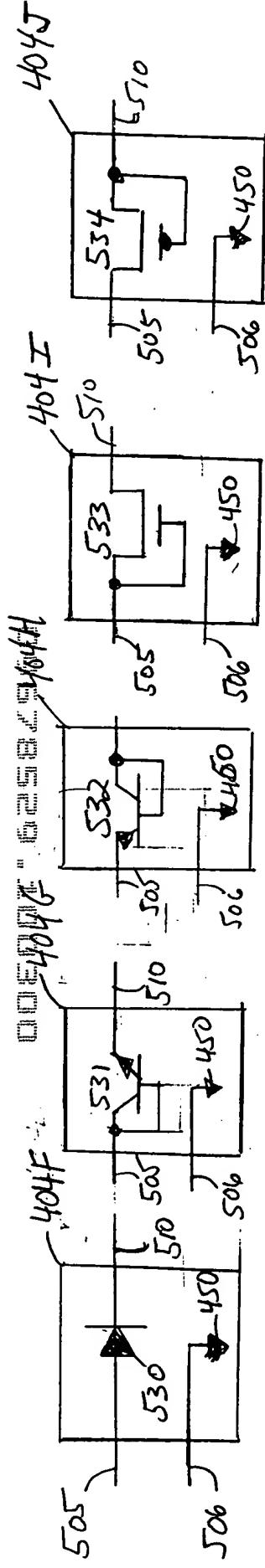


FIG. 5A

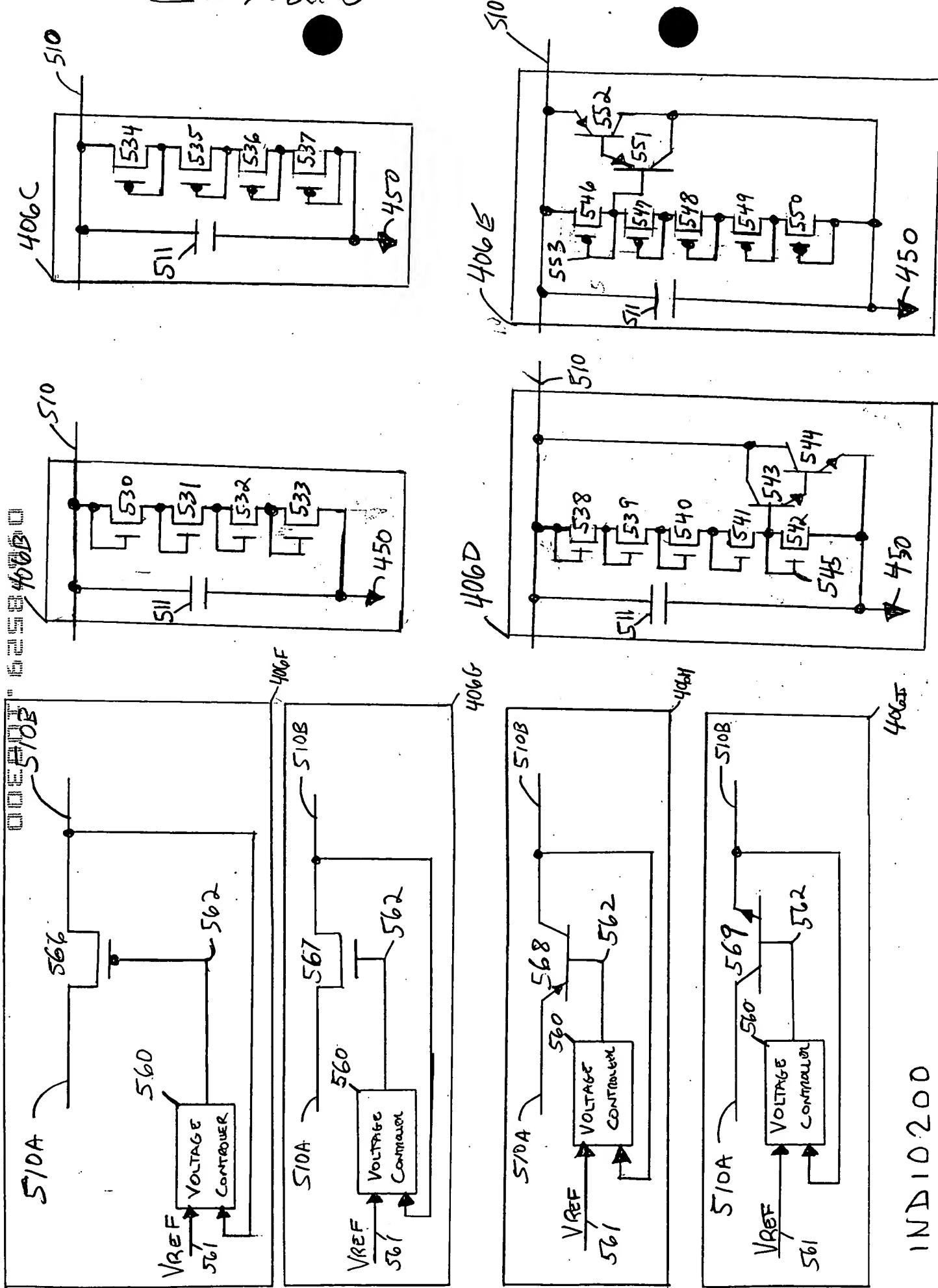
IND 10200
Sh. 6 of 16

IND10200



IND10200 SH. 7 of 16

Fig. 5 B



IND 10.200
sh. 8 of 16

IND 10200

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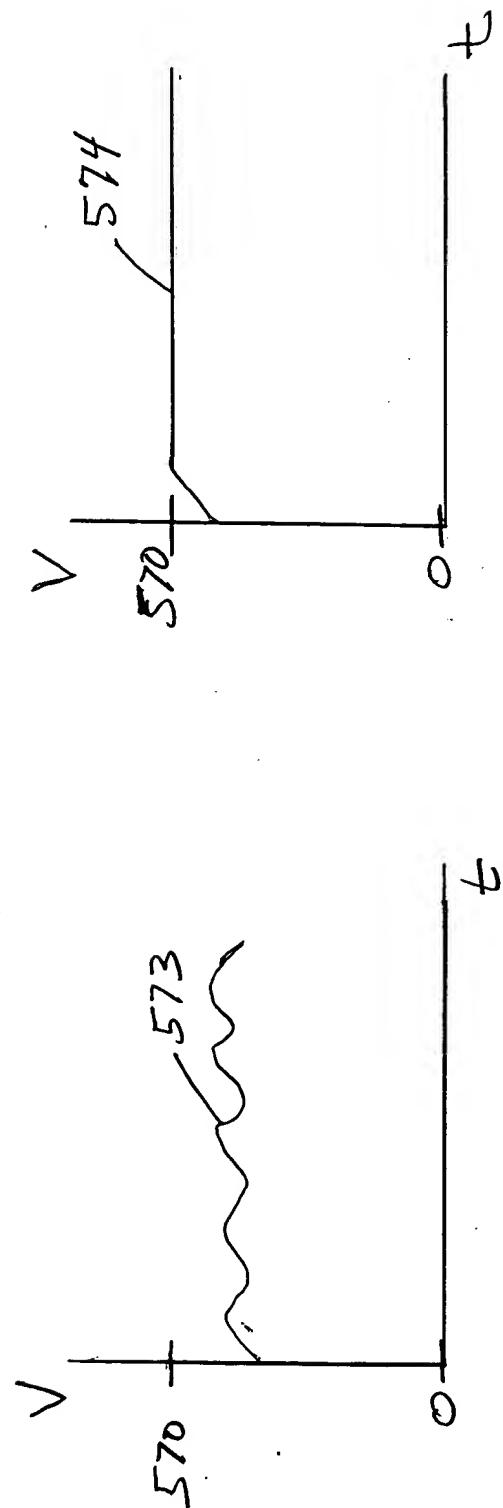
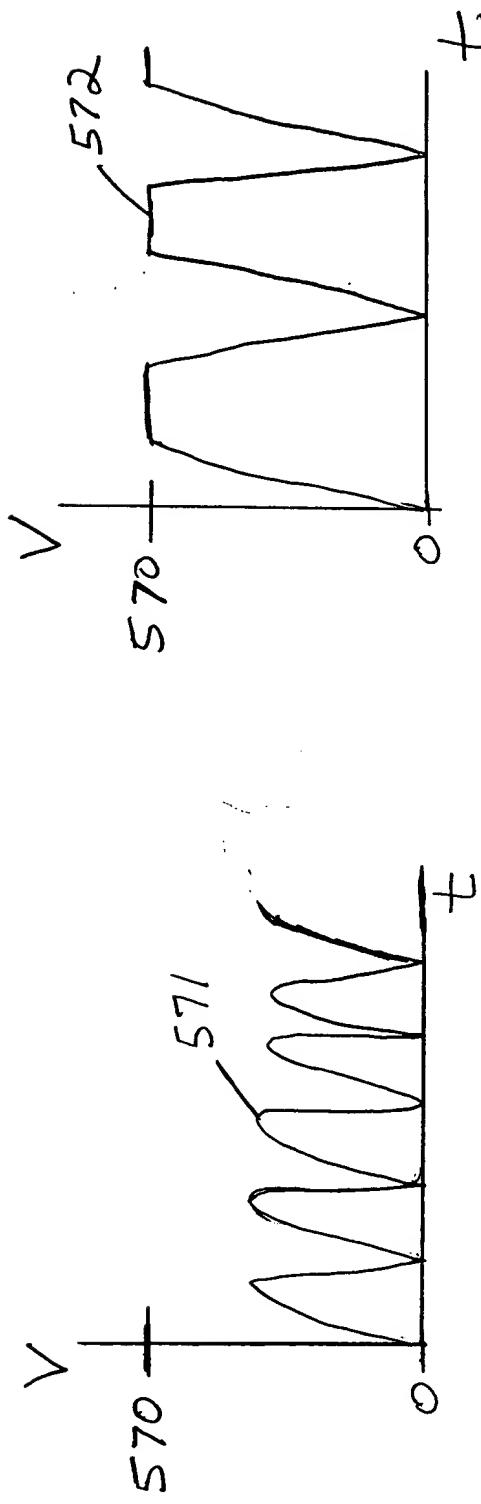


FIG. 5D

IND 10200
sh. 9 of 16

IND 10200

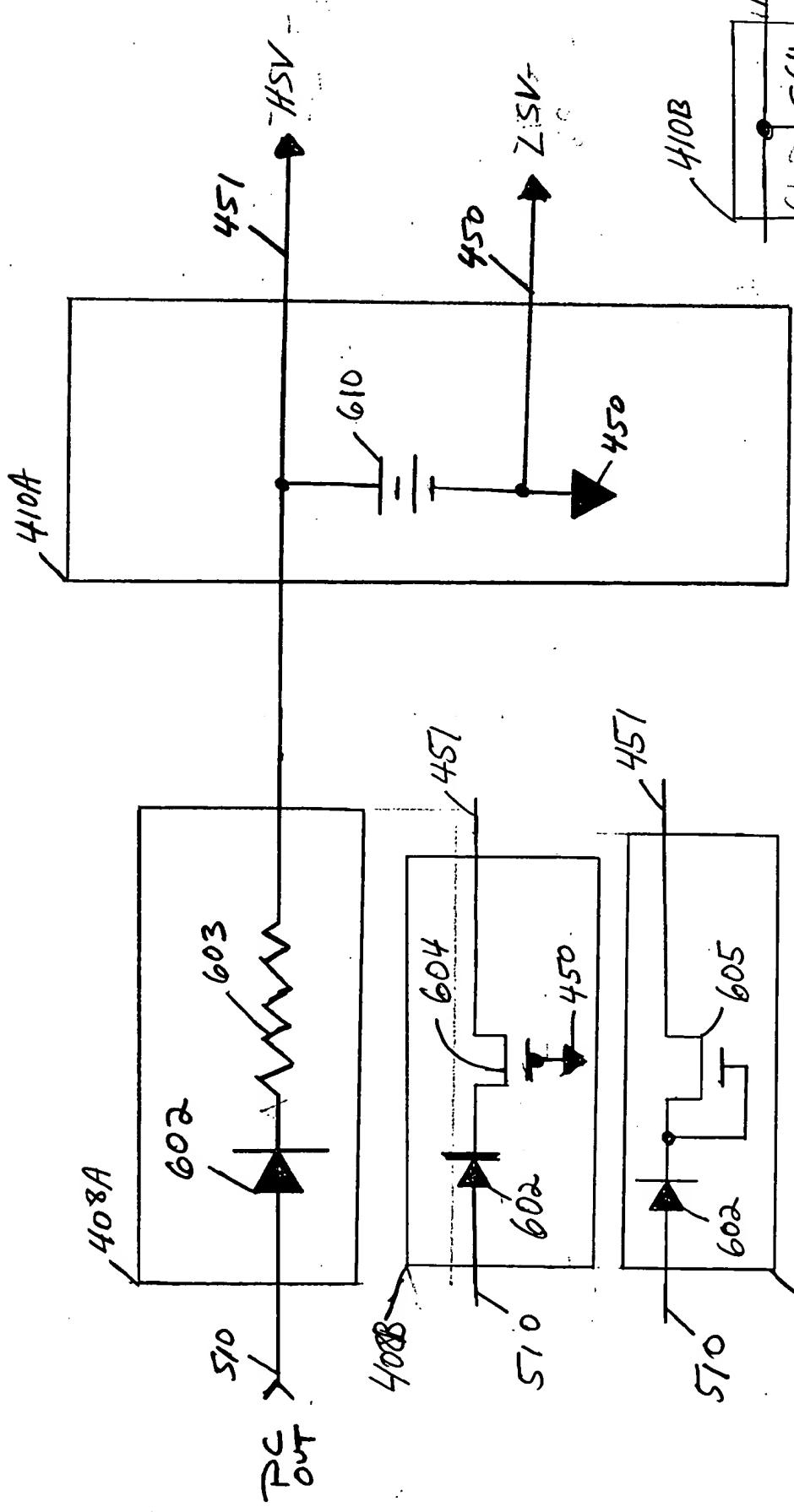


Fig. 6A

IND 10200
sh. 10 of 16

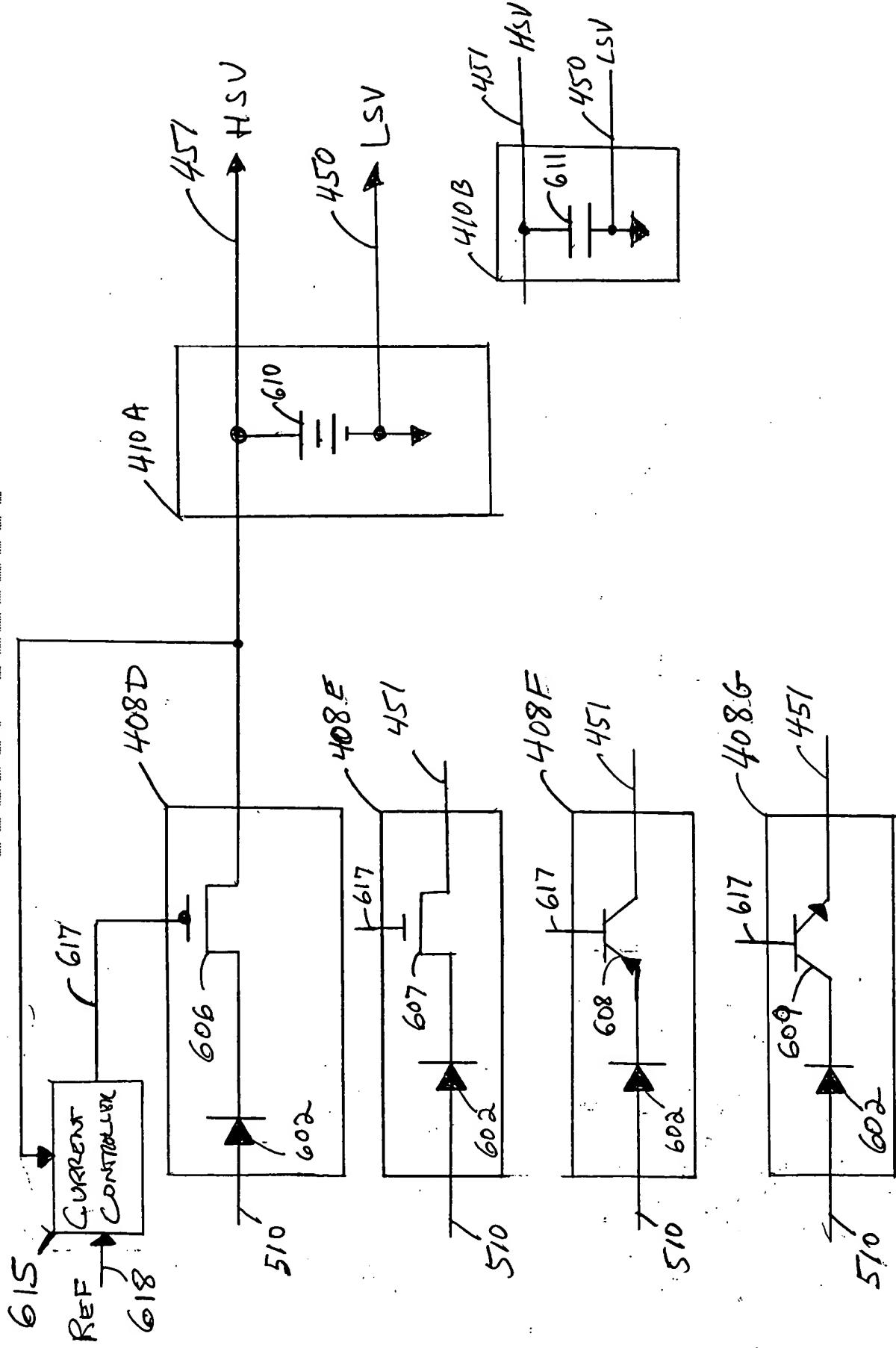
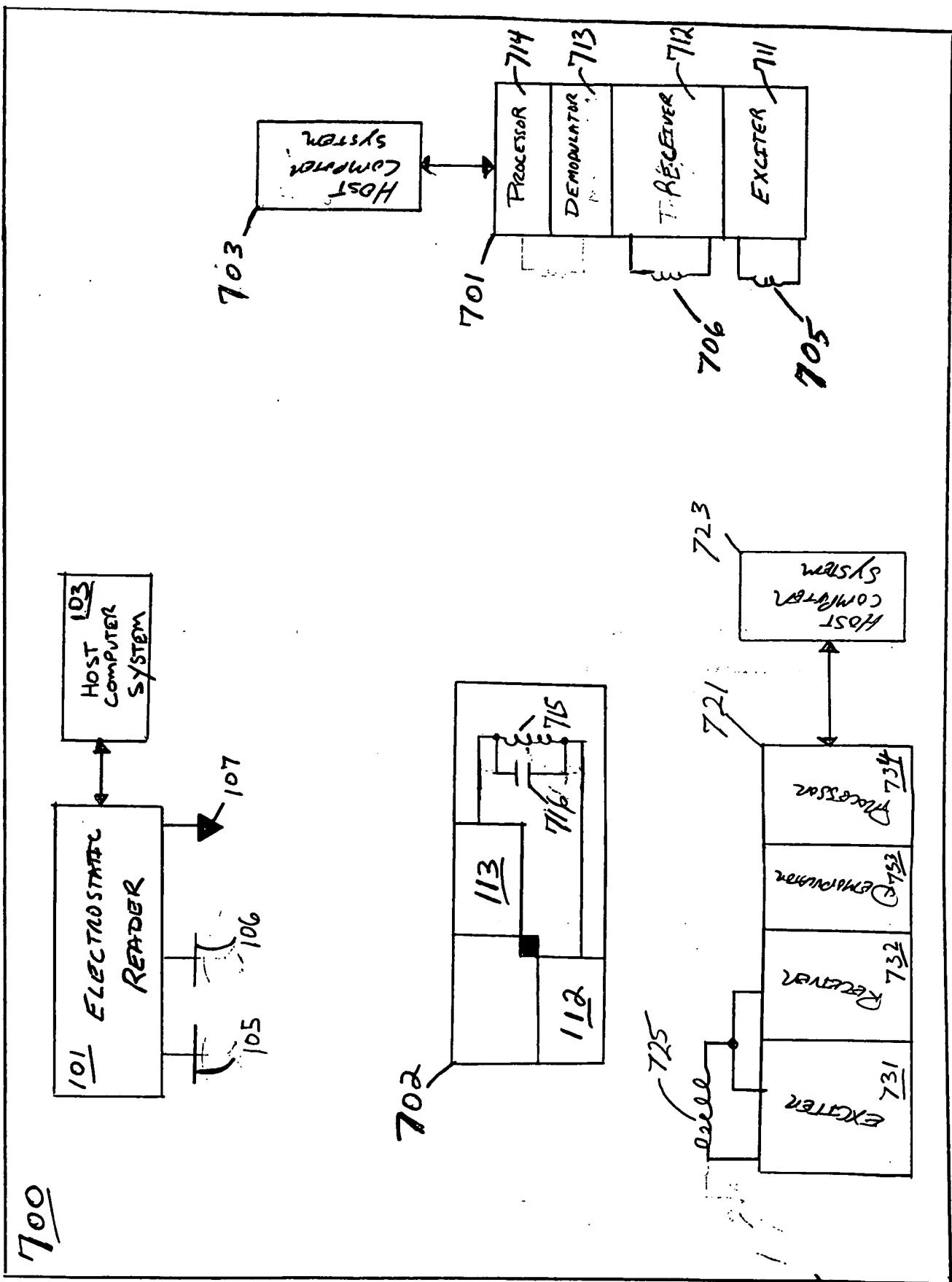
IND10200
sh. 11 of 16

FIG. 6B

END 10200



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sh. 12 of 16

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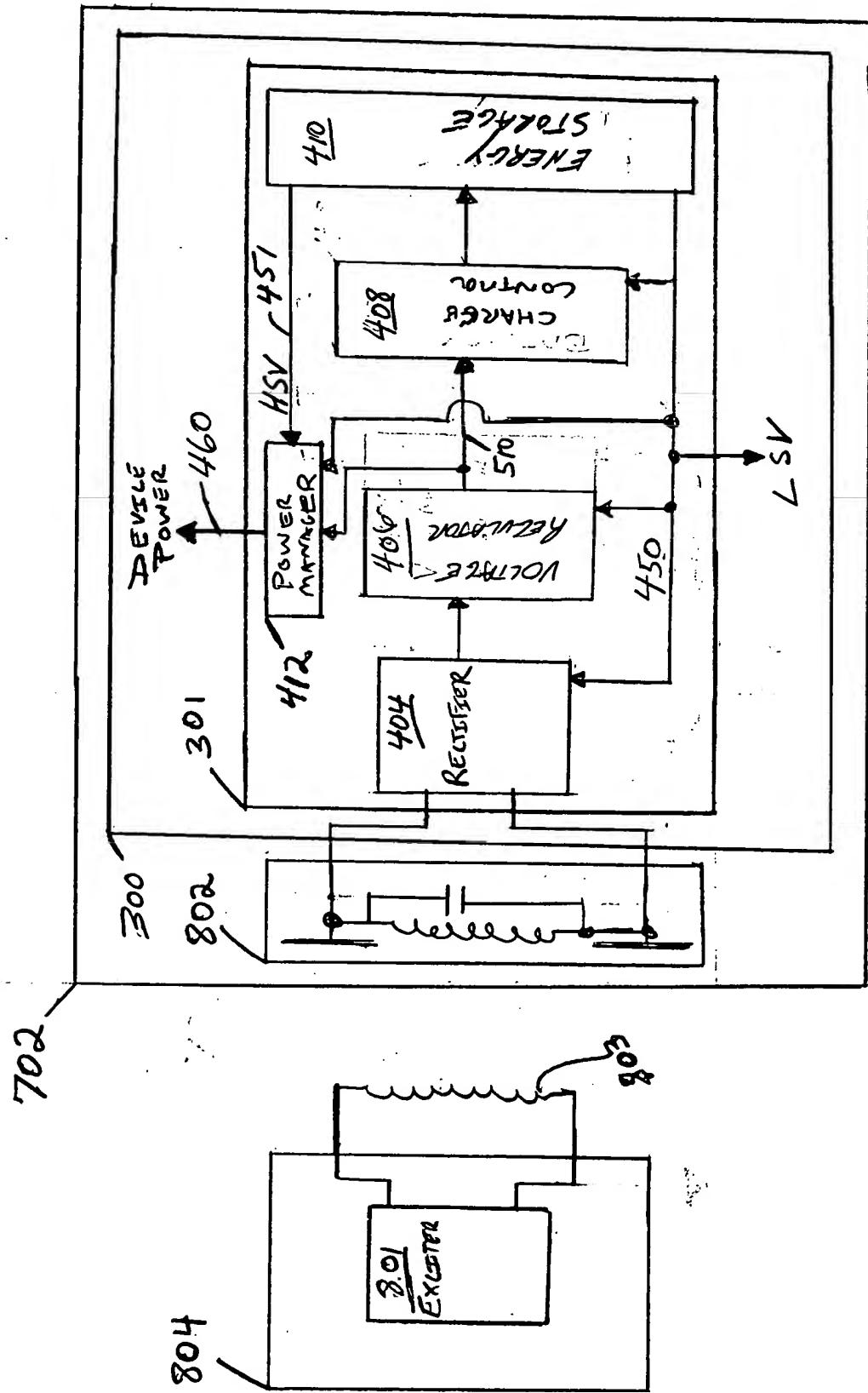


Fig. 8A

IND 10200
sh. 13 of 16

END 10200

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402

410

HSV-451

POWER MATE 412

460

Evenly spaced

150

✓ SV. A

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LLock

309X
MODULE

500

406x
VOLTRAC
REGISTRATION

RECEIVED
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230715

310 *Memory*

30% **30%** **30%**
MVR **MVR** **MVR**
MAX **MAX** **MAX**

→ Control on 307

3091
Modulator

4061
VOL 1846
FEB 11 1946
Soy

4041
DISCUSSION

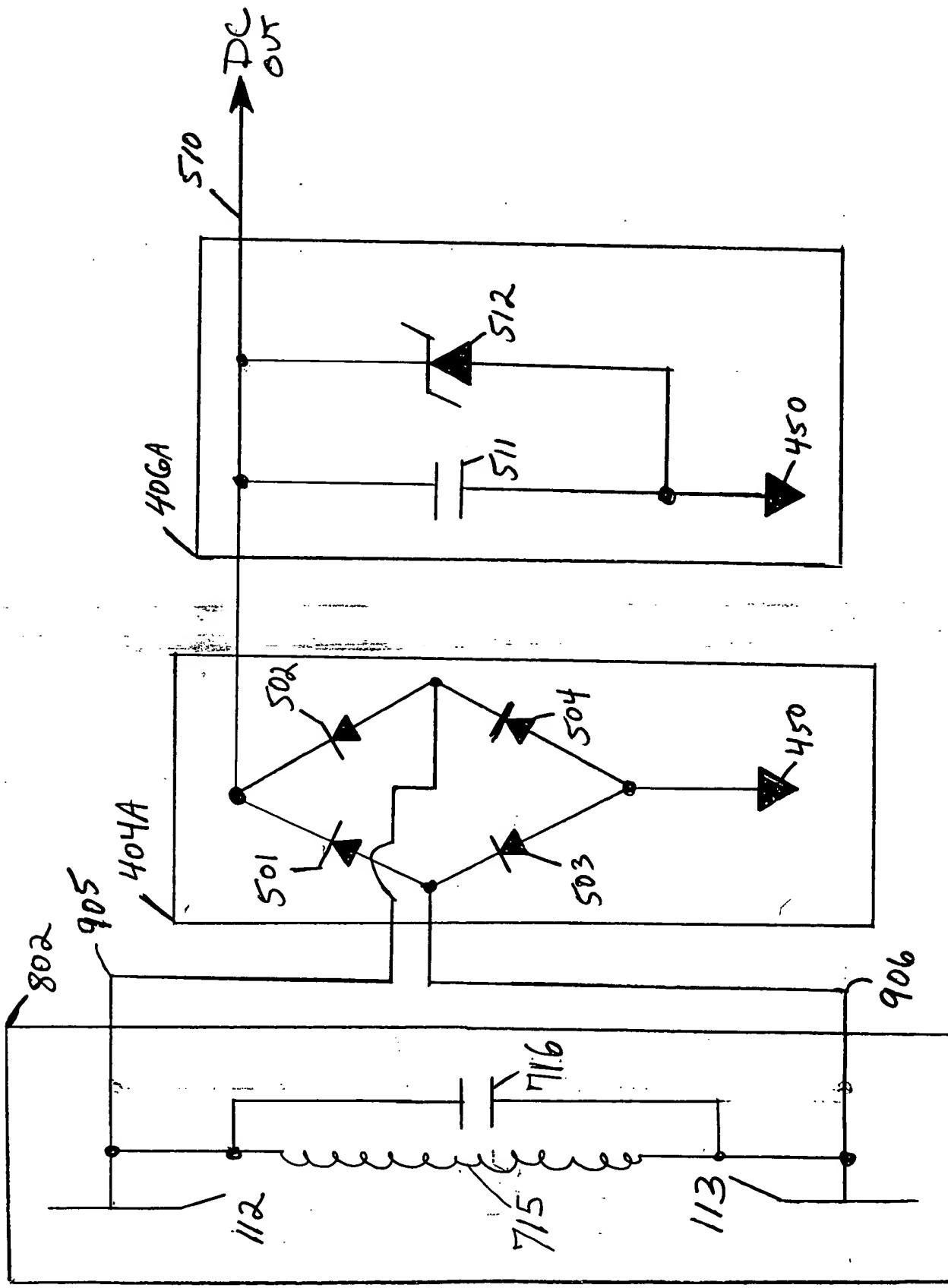
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FIG. 8 B

IND10200

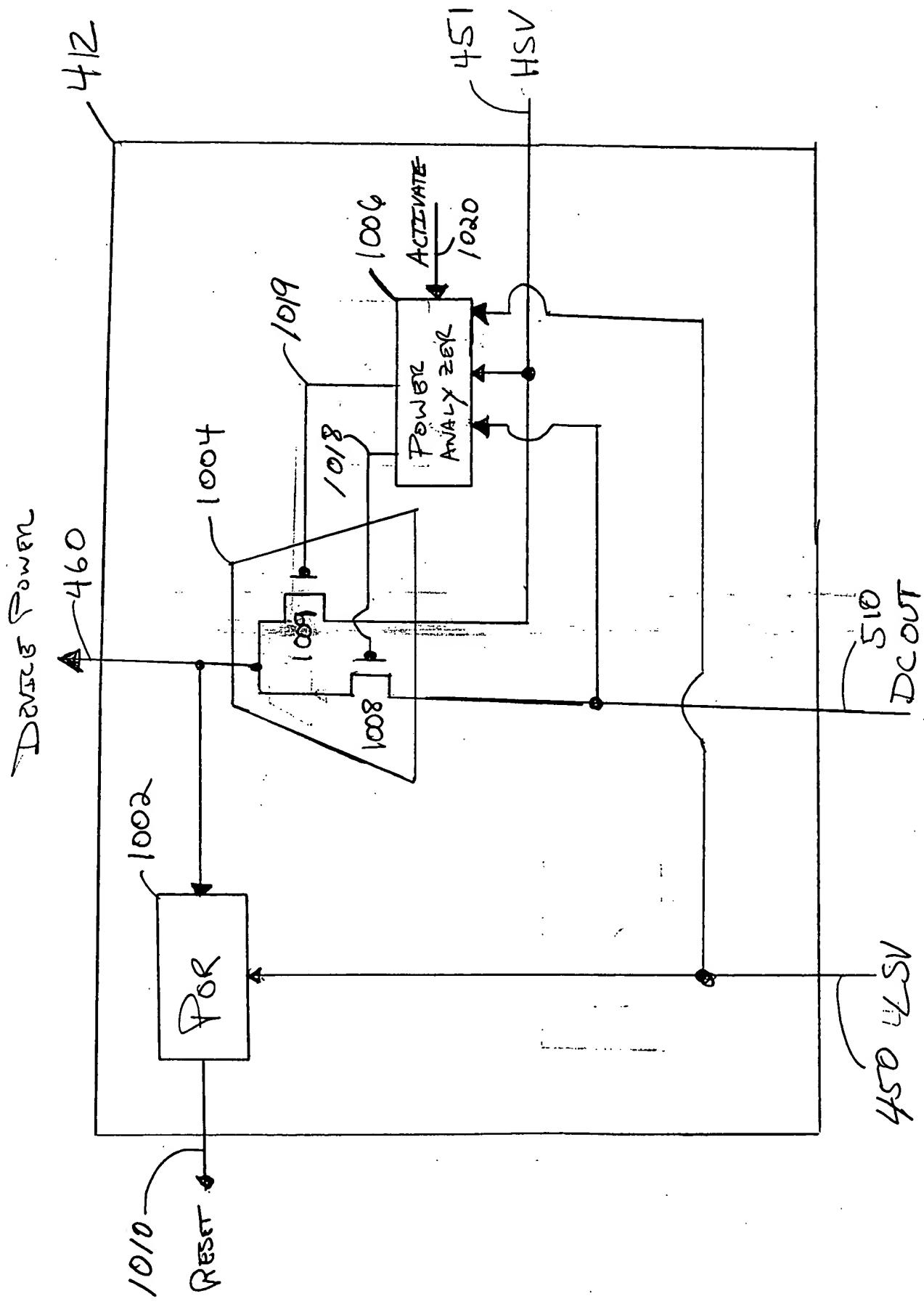
000001 02582950



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FIG.



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IND 10200

FIG. 10